

CS 5090: Software Fault Tolerance

Automated Program Synthesis

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S/W Fault-Tolerance - Ebneenasir - Spring 2008

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What is automatic program synthesis?

```
graph LR; A["Specification  
(expressed in terms of a formal language)"] --> B["Synthesis  
Algorithm"]; B --> C["Synthesized  
Program Model  
(An abstract structure)"]
```

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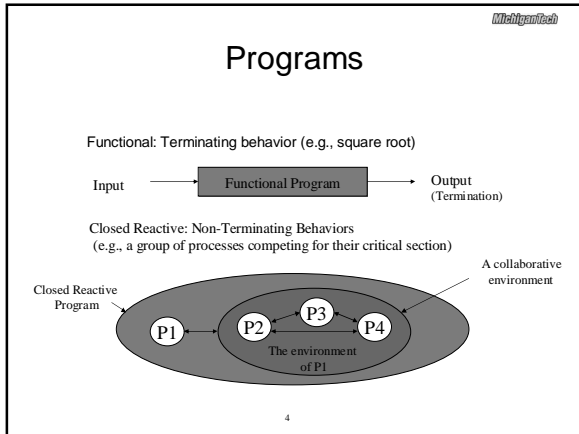
Why automatic program synthesis?

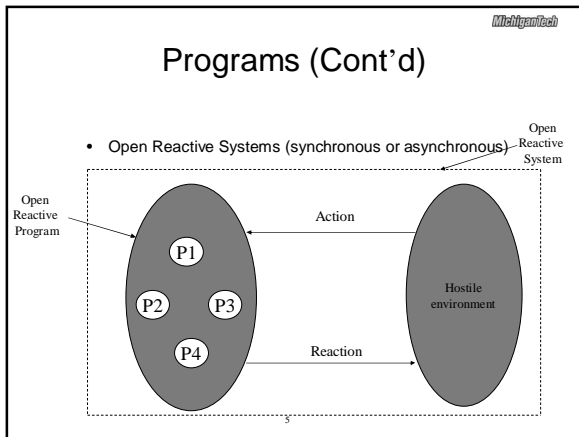
- Verification (especially model checking):
 - Checking a property with respect to a program model
 - Develop a program model
 - Check the model with respect to specified properties
 - Drawback: verification after the design

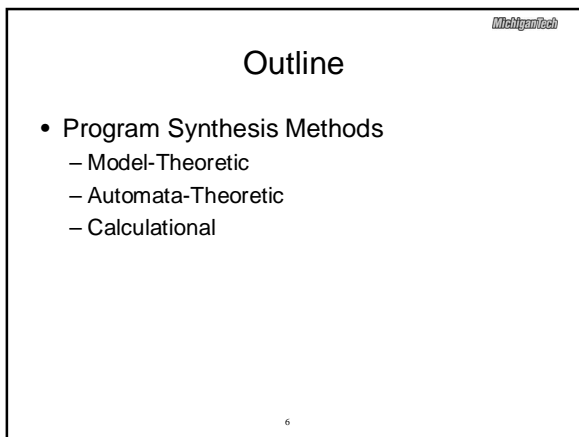
Question:
Does there exist an algorithm to design a program from its specification?

Benefit: Correct by construction

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Model-Theoretic Approach

- Automatic synthesis of closed reactive programs

```

    graph TD
      A[Temporal specification] --> B{Is the specification satisfiable?  
(tableau proof)}
      B -- Yes --> C[Create a finite model of the specification]
      C -- Finite model --> D[Extract the synchronization skeleton  
of each process]
      B -- No --> E([The specification  
is not satisfiable])
  
```

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Model-Theoretic Approach - Issues

- Specification Language
 - Variants of propositional temporal logic
- Program Model
 - Shared memory [Emerson&Clarke 1982]
 - Message passing [Manna&Wolper 1984]
- Decision procedure
 - Tableau-based proof
- Complexity
 - Exponential in the length of the specification
- Distribution
 - Decomposition of test-and-set actions (high atomicity) into atomic (low atomicity) read/write actions [Emerson&Clarke 2001]

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Model-Theoretic Approach - Example

- Mutual Exclusion problem [Emerson&Clarke 1982]
- Problem Specification ($i = 1, 2$):
 - Start state; $NC S_1 \wedge NC S_2$.
 - Mutual exclusion; $AG(\neg(CS_1 \wedge CS_2))$.
 - Progress; $AG(TRY_i \Rightarrow AFC S_i)$.
- Invariants:
 - $AG(NC S_i \vee TRY_i \vee CS_i)$,
 - $AG(NC S_i \Rightarrow \neg(TRY_i \vee CS_i))$,
 - $AG(TRY_i \Rightarrow \neg(NC S_i \vee CS_i))$,
 - $AG(CS_i \Rightarrow \neg(TRY_i \vee NC S_i))$.

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Model-Theoretic Approach - Example

- Structural Specification:

$$(AG(NCS_i \Rightarrow (AX_iTRY_i \wedge EX_iTRY_i)))$$
- No process interferes with the transitions of the other process

$$AG(NCS_i \Rightarrow AX_jNCS_i),$$

$$AG(TRY_i \Rightarrow AX_jTRY_i),$$

$$AG(CS_i \Rightarrow AX_jCS_i).$$

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Model-Theoretic Approach – Example (continued)

- Synthesis method
 - Build a tableau proof of the spec.
 - Extract a finite model
 - Extract the synchronization skeleton of each process

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Model-Theoretic Approach – Example (Reduction Rules)

- Conjunctive rules:

$\alpha = f \wedge g$	$\alpha_1 = f$	$\alpha_2 = g$
$\alpha = AGg$	$\alpha_1 = g$	$\alpha_2 = AXAGg$
$\alpha = EGg$	$\alpha_1 = g$	$\alpha_2 = EXEGg$
- Disjunctive rules

$\beta = f \vee g$	$\beta_1 = f$	$\beta_2 = g$
$\beta = AFg$	$\beta_1 = g$	$\beta_2 = AXAFg$
$\beta = EFg$	$\beta_1 = g$	$\beta_2 = EXEFg$

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Model-Theoretic Approach – Example (continued)

- A tableau for the initial node

$$\begin{array}{c}
 \text{NCS}_1 \\
 \downarrow \\
 \text{AG}(\text{AX}_1 \text{TRY}_1 \wedge \text{EX}_1 \text{TRY}_1) \\
 \downarrow \\
 \text{AG}(\text{AX}_2 \text{NCS}_1) \\
 \downarrow \\
 \text{AX}_1 \text{TRY}_1 \wedge \text{EX}_1 \text{TRY}_1 \\
 \downarrow \\
 \text{A Block corresponding to each leaf in the tableau}
 \end{array}$$

$$\begin{array}{c}
 \text{AX}_1 \text{TRY}_1 \\
 \downarrow \\
 \text{EX}_1 \text{TRY}_1 \\
 \downarrow \\
 \text{AX}_2 \text{NCS}_1 \\
 \downarrow \\
 \text{Leaf}
 \end{array}$$

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Model-Theoretic Approach – Example (continued)

$$\begin{array}{c}
 \checkmark \text{NCS}_1 \\
 \downarrow \\
 \text{AG}(\text{AX}_1 \text{TRY}_1 \wedge \text{EX}_1 \text{TRY}_1) \\
 \downarrow \\
 \text{AG}(\text{AX}_2 \text{NCS}_2) \\
 \downarrow \\
 \text{AX}_1 \text{TRY}_1 \wedge \text{EX}_1 \text{TRY}_1 \\
 \downarrow \\
 \checkmark \text{AX}_1 \text{TRY}_1 \\
 \downarrow \\
 \checkmark \text{EX}_1 \text{TRY}_1 \\
 \downarrow \\
 \checkmark \text{AX}_2 \text{NCS}_1
 \end{array}$$

$$\begin{array}{c}
 \checkmark \text{NCS}_2 \\
 \downarrow \\
 \text{AG}(\text{AX}_2 \text{TRY}_2 \wedge \text{EX}_2 \text{TRY}_2) \\
 \downarrow \\
 \text{AG}(\text{AX}_1 \text{NCS}_2) \\
 \downarrow \\
 \text{AX}_2 \text{TRY}_2 \wedge \text{EX}_2 \text{TRY}_2 \\
 \downarrow \\
 \checkmark \text{AX}_2 \text{TRY}_2 \\
 \downarrow \\
 \checkmark \text{EX}_2 \text{TRY}_2 \\
 \downarrow \\
 \checkmark \text{AX}_1 \text{NCS}_2
 \end{array}$$

The reduced tableau for NCS_1 and NCS_2 .
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Model-Theoretic Approach – Example (continued)

- Blocks($\text{NCS}_1 \wedge \text{NCS}_2$)

D:

$\text{NCS}_1 \quad \text{NCS}_2$

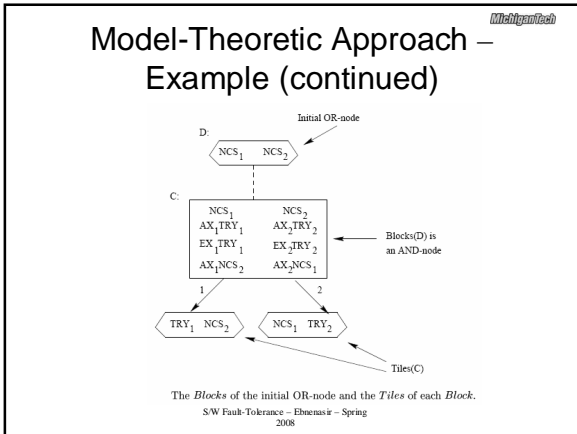
Initial OR-node

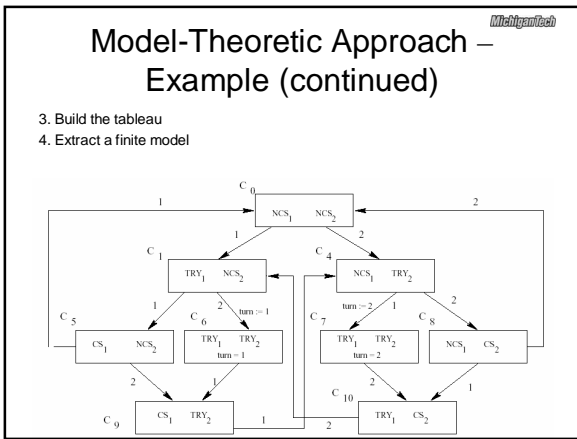
C:

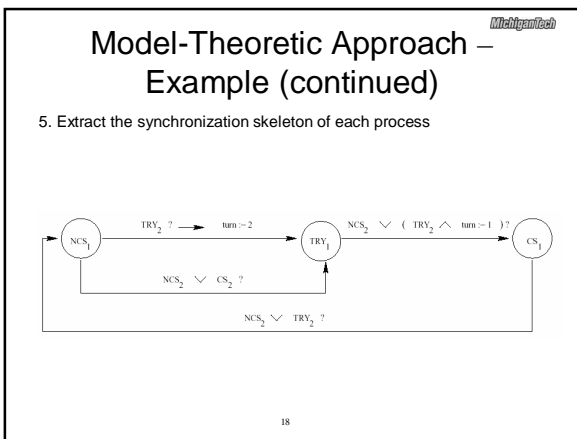
NCS_1	NCS_2
$\text{AX}_1 \text{TRY}_1$	$\text{AX}_2 \text{TRY}_2$
$\text{EX}_1 \text{TRY}_1$	$\text{EX}_2 \text{TRY}_2$
$\text{AX}_1 \text{NCS}_2$	$\text{AX}_2 \text{NCS}_1$

Blocks(D) is an AND-node

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Model-Theoretic Approach – Some References

- E. Allen Emerson, Edmund M. Clarke: Using Branching Time Temporal Logic to Synthesize Synchronization Skeletons. *Sci. Comput. Program.* 2(3): 241-266 (1982)
- Paul C. Attie, E. Allen Emerson: Synthesis of Concurrent Systems with Many Similar Processes. *ACM Trans. Program. Lang. Syst.* 20(1): 51-115 (1998)
- Paul C. Attie, E. Allen Emerson: Synthesis of concurrent programs for an atomic read/write model of computation. *ACM Trans. Program. Lang. Syst.* 23(2): 187-242 (2001)
- Zohar Manna, Pierre Wolper: Synthesis of Communicating Processes from Temporal Logic Specifications. *ACM Trans. Program. Lang. Syst.* 6(1): 68-93 (1984)

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Automata-Theoretic Approach

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Automata-Theoretic Approach

- Automatic synthesis of open reactive programs
 - Shared-variable synchronous open reactive programs
 - Shared-variable asynchronous open reactive programs

Specification:
linear temporal logic
formula $\varphi(x, y)$

The diagram illustrates the interaction between a 'Single-Process Program' (represented by a grey oval) and a 'Non-deterministic environment' (represented by a grey oval). They are connected by two shared variables, 'x' and 'y', which are enclosed in a dashed rectangular box labeled 'Shared variables'. Arrows indicate the flow of information: one arrow points from the environment to the program for variable 'x', and another arrow points from the program to the environment for variable 'y'.

Pnueli & Rosner 1989

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Automata-Theoretic Approach – Basic Concepts

- Open reactive system computations

Environment writes on x

Program reads x and writes y

Pnueli & Rosner 1989

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Automata-Theoretic Approach – Synthesis Method

- Synthesis method

Linear temporal specification

Pnueli & Rosner 1989

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Automata-Theoretic Approach – Issues

- Specification language
 - Temporal formula → Tree automaton
- Program model
 - Single-process program
 - Concurrent processes on a distributed architecture
- Synthesis method
 - A sequence of conversions on the specification
 - Reducing the synthesis problem to non-emptiness checking problem
- Complexity
 - Single-process program: Doubly exponential in the length of specification
 - Distributed programs with arbitrary architecture: Undecidable [Pnueli & Rosner 1990]

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Automata-Theoretic Approach –
Some References

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- Amir Pnueli, Roni Rosner: On the Synthesis of an Asynchronous Reactive Module. ICALP 1989: 652-671
- Amir Pnueli, Roni Rosner: On the Synthesis of a Reactive Module. POPL 1989: 179-190
- Amir Pnueli, Roni Rosner: Distributed Reactive Systems Are Hard to Synthesize FOCS 1990: 746-757
- Orna Kupferman, Moshe Y. Vardi: Synthesizing Distributed Systems. LICS 2001
