Reignition and Restrikes

By :- Himanshu Bahirat EE5220 : Power System Transients Date:- February 22, 2010

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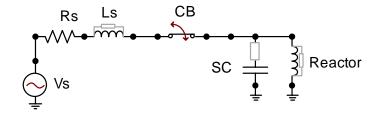
- Normal and Abnormal Transients
- Reignition
 - Reactor Switching
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 - Capacitor switching
 - Three phase capacitor switching
 - Grounded vs. Ungrounded banks

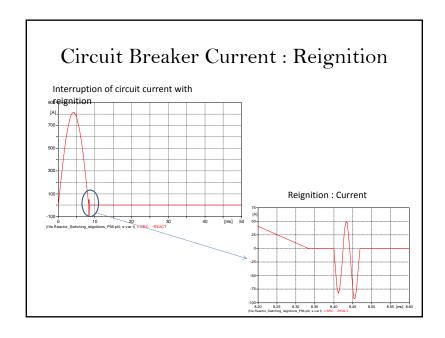
Normal and Abnormal Transients

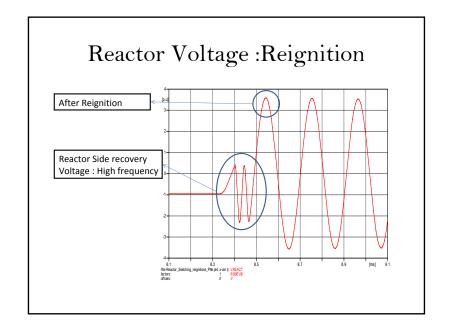
- Normal Transients
 - Transients during energization
 - Up to 2 p.u. voltages
 - No trapped charges assumed
 - Capacitor Energization inrush currents and voltages
- Abnormal Transients
 - During denergization
 - Trapped charges
 - Very high voltages and currents
 - Current Chopping
 - Interruption of currents
 - Restrikes and Reignitions

Reactor Switching

- Trapped Energy
- High Frequency oscillations of reactor voltage
- Dielectric Breakdown



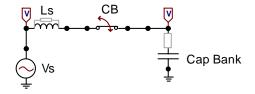




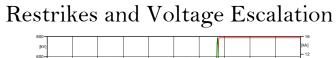
Reignition : Definition A reignition occurs when a current is interrupted at current zero and then re-establishes itself within one-eight of a power frequency cycle [2]. Reignition Re

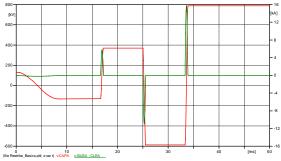
Capacitor Switching

- Switching of normal load current
- Deenergization at system peak voltage
- Trapped charge on the capacitor

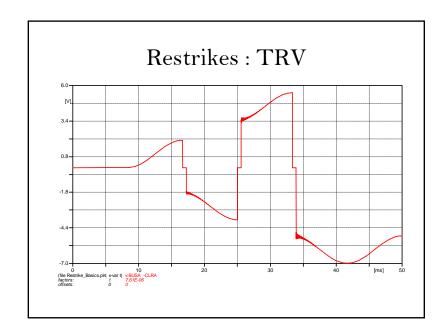


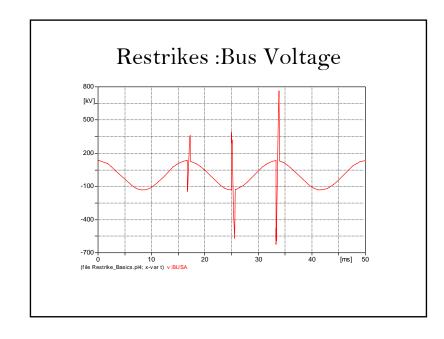
Capacitor Bank Trapped Voltage and TRV Trapped Voltage on Capacitor Bank after deenergization Circuit Breaker TRV **Trapped Voltage on Capacitor Bank after deenergization **Gircuit Breaker TRV **Trapped Voltage on Capacitor Bank after deenergization **Gircuit Breaker TRV **Trapped Voltage on Capacitor Bank after deenergization **Gircuit Breaker TRV **Trapped Voltage on Capacitor Bank after deenergization **Gircuit Breaker TRV **Trapped Voltage on Capacitor Bank after deenergization **Gircuit Breaker TRV **Trapped Voltage on Capacitor Bank after deenergization **Gircuit Breaker TRV **Trapped Voltage on Capacitor Bank after deenergization **Gircuit Breaker TRV **Trapped Voltage on Capacitor Bank after deenergization **Gircuit Breaker TRV **Trapped Voltage on Capacitor Bank after deenergization **Gircuit Breaker TRV **Trapped Voltage on Capacitor Bank after deenergization **Gircuit Breaker TRV **Trapped Voltage on Capacitor Bank after deenergization **Gircuit Breaker TRV **Trapped Voltage on Capacitor Bank after deenergization **Gircuit Breaker TRV **Trapped Voltage on Capacitor Bank after deenergization **Gircuit Breaker TRV **Trapped Voltage on Capacitor Bank after deenergization **Gircuit Breaker TRV **Trapped Voltage on Capacitor Bank after deenergization **Gircuit Breaker TRV **Trapped Voltage on Capacitor Bank after deenergization **Gircuit Breaker TRV **Trapped Voltage on Capacitor Bank after deenergization **Gircuit Breaker TRV **Trapped Voltage on Capacitor Bank after deenergization **Gircuit Breaker TRV **Trapped Voltage on Capacitor Bank after deenergization **Gircuit Breaker TRV **Trapped Voltage on Capacitor Bank after deenergization **Gircuit Breaker TRV **Trapped Voltage on Capacitor Bank after deenergization **Gircuit Breaker TRV **Trapped Voltage on Capacitor Bank after deenergization **Trapped Voltage on Capacitor Bank after deenergization **Trapped Voltage on Capacitor Bank after deenergization **Trapped Voltage on Capacitor Bank after deenergizati



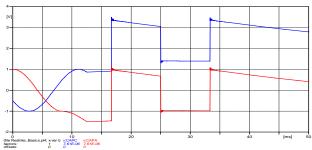


- Voltage escalation with multiple restrikes
- Stored Voltage on Capacitor Bank increase with each restrike. (1st -3.p.u. $2^{\rm nd}-5$ p.u
- Very high transient current spikes



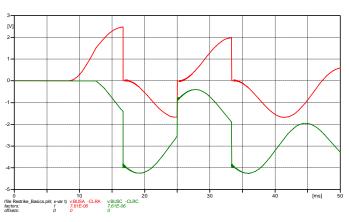


Restrikes :Ungrounded banks Capacitor Bank Voltage



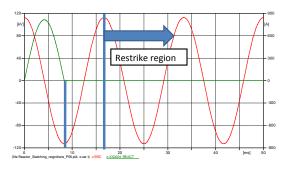
- Higher trapped voltages than the case of grounded banks.
- Restrike on phase A results in escalation on phase c.
- First restrike trapped voltage increases to about 3.5 p.u

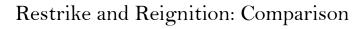
Restrikes :Ungrounded banks Circuit Breaker TRV

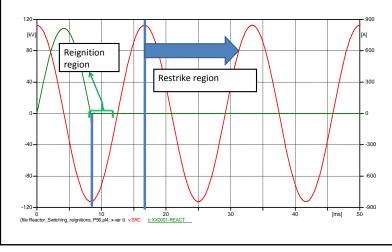


Restrike: Definition

The term "restrike" is defined as a re-establishment of the current, one-quarter cycle or longer, following interruption of a capacitive current at a normal current zero [1].







Conclusions

- Abnormal transients are a result of trapped charges and energy.
- Reignitions and restrikes can lead to very magnitude transients.
- Multiple restrikes can damage circuit breakers and other equipments.
- Ungrounded Banks result in higher magnitude of transients than grounded banks.
- Can controlled with use of surge arresters.

References

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